



PATENT
450111-04806

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yugang MA
Serial No. : 10/662,739
Filed : September 15, 2003
For : CHANNEL EQUALISATION
Art Unit : 2661

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Bruno Polito, Reg. No. 38,580

Name of Applicant, Assignee or
Registered Representative

Signature

February 3, 2004

Date of Signature

CLAIM OF PRIORITY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In support of the claim of priority under 35. U.S.C.
§ 119 asserted in the Declaration accompanying the above-entitled
application, as filed, please find enclosed herewith a certified
copy of Singapore Application No. SG200205830-3, filed in
Singapore on 17 September 2002 forming the basis for such claim.

PATENT
450111-04806

Acknowledgment of the claim of priority and of the
receipt of said certified copy(s) is requested.

Respectfully submitted,

FROMMER LAWRENCE & HAUG LLP
Attorneys for Applicant

By: B. Polito
Bruno Polito
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Enclosure(s)

**REGISTRY OF PATENTS
SINGAPORE**

This is to certify that the annexed is a true copy of specification as filed for the following Singapore patent application.

Date of Filing : 17 SEPTEMBER 2002

Application Number : 200205830-3

Applicant(s) /
Proprietor(s) of
Patent : SONY ELECTRONICS (SINGAPORE)
PTE LTD

Title of Invention : CHANNEL EQUALISATION



SHARMAINE WU (Ms)
Assistant Registrar

for REGISTRAR OF PATENTS

PATENTS FORM 1

Patents Act
(Cap. 221)
Patents Rules
Rule 19

INTELLECTUAL PROPERTY OFFICE OF SINGAPORE

**REQUEST FOR THE GRANT OF A PATENT UNDER
SECTION 25**

101101

* denotes mandatory fields

1. YOUR REFERENCE*

SP5099

**2. TITLE OF
INVENTION***

CHANNEL EQUALISATION

3. DETAILS OF APPLICANT(S)* (see note 3)

Number of applicant(s)

1

(A) Name

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State

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SG

☒

For corporate applicant

☐

For individual applicant

State of incorporation

State of residency

Country of incorporation

SG

Country of residency

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(B) Name

Address

State

Country



☐ For corporate applicant

☐ For individual applicant

State of incorporation

State of residency

Country of incorporation

Country of residency

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(C) Name

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State

Country

☐ For corporate applicant

☐ For individual applicant

State of incorporation

State of residency

Country of incorporation

Country of residency

☐ For others (please specify in the box provided below)

☐

Further applicants are to be indicated on continuation sheet 1

4. DECLARATION OF PRIORITY (see note 5)

A. Country/country designated

DD MM YYYY

File number

Filing Date

B. Country/country designated

DD MM YYYY

File number

Filing Date

☐

Further details are to be indicated on continuation sheet 6

5. INVENTOR(S)* (see note 6)

A. The applicant(s) is/are the sole/joint inventor(s)

Yes

☐

No

☒

17 SEP 2002
200205830-3

B. A statement on Patents Form 8 is/will be furnished

Yes

☒

No

☐

6. CLAIMING AN EARLIER FILING DATE UNDER (see note 7)

☐

section 20(3)

☐

section 26(6)

☐

section 47(4)

Patent application number

DD MM YYYY

Filing Date

Please mark with a cross in the relevant checkbox provided below
(Note: Only one checkbox may be crossed.)

☐

Proceedings under rule 27(1)(a)

DD MM YYYY

Date on which the earlier application was amended

☐

Proceedings under rule 27(1)(b)

7. SECTION 14(4)(C) REQUIREMENTS (see note 8)

Invention has been displayed at an international exhibition. Yes

☐

No

☒

8. SECTION 114 REQUIREMENTS (see note 9)

The invention relates to and/or used a micro-organism deposited for the purposes of disclosure in accordance with section 114 with a depository authority under the Budapest Treaty.

Yes

☐

No

☒

9. CHECKLIST*

(A) The application consists of the following number of sheets

i. Request

5

Sheets

ii. Description

19

Sheets

iii. Claim(s)

4

Sheets

iv. Drawing(s)

6

Sheets

v. Abstract

(Note: The figure of the drawing, if any, should accompany the abstract)

1

Sheets

Total number of sheets

35

Sheets

(B) The application as filed is accompanied by:

☐

Priority document(s)

☐

Translation of priority document(s)

☒

Statement of inventorship
& right to grant

☐

International exhibition certificate

10. DETAILS OF AGENT (see notes 10, 11 and 12)

Name

Firm

LLOYD WISE

11. ADDRESS FOR SERVICE IN SINGAPORE* (see note 10)

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Postal Code

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12. NAME, SIGNATURE AND DECLARATION (WHERE APPROPRIATE) OF APPLICANT OR AGENT* (see note 12)

(Note: Please cross the box below where appropriate.)

☒

I, the undersigned, do hereby declare that I have been duly authorised to act as representative, for the purposes of this application, on behalf of the applicant(s) named in paragraph 3 herein.

Lloyd Wise
Name and Signature LLOYD WISE

DD MM YYYY

17 09 2002

Our Ref: SP5099

NOTES:

1. This form when completed, should be brought or sent to the Registry of Patents together with the rest of the application. Please note that the filing fee should be furnished within the period prescribed.
2. The relevant checkboxes as indicated in bold should be marked with a cross where applicable.
3. Enter the name and address of each applicant in the spaces provided in paragraph 3.
Where the applicant is an individual
 - Names of individuals should be indicated in full and the surname or family name should be underlined.
 - The address of each individual should also be furnished in the space provided.
 - The checkbox for "For individual applicant" should be marked with a cross.Where the applicant is a body corporate
 - Bodies corporate should be designated by their corporate name and country of incorporation and, where appropriate, the state of incorporation within that country should be entered where provided.
 - The address of the body corporate should also be furnished in the space provided.
 - The checkbox for "For corporate applicant" should be marked with a cross.Where the applicant is a partnership
 - The details of all partners must be provided. The name of each partner should be indicated in full and the surname or family name should be underlined.
 - The address of each partner should also be furnished in the space provided.
 - The checkbox for "For others" should be marked with a cross and the name and address of the partnership should be indicated in the box provided.
4. In the field for "Country", please refer to the standard list of country codes made available by the Registry of Patents and enter the country code corresponding to the country in question.
5. The declaration of priority in paragraph 4 should state the date of the previous filing, the country in which it was made, and indicate the file number, if available. Where the application relied upon in an International Application or a regional patent application e.g. European patent application, one of the countries designated in that application [being one falling under section 17 of the Patents Act] should be identified and the country should be entered in the space provided.
6. Where the applicant or applicants is/are the sole inventor or the joint inventors, paragraph 5 should be completed by marking with a cross the 'YES' checkbox in the declaration (A) and the 'NO' checkbox in the alternative statement (B). Where this is not the case, the 'NO' checkbox in declaration (A) should be marked with a cross and a statement will be required to be filed on Patents Form 8.
7. When an application is made by virtue of section 20(3), 26(6) or 47(4), the appropriate section should be identified in paragraph 6 and the number of the earlier application or any patent granted thereon identified. Applicants proceeding under section 26(6) should identify which provision in rule 27 they are proceeding under. If the applicants are proceeding under rule 27(1)(a), they should also indicate the date on which the earlier application was amended.
8. Where the applicant wishes an earlier disclosure of the invention by him at an International Exhibition to be disregarded in accordance with section 14(4)(c), then the 'YES' checkbox at paragraph 7 should be marked with a cross. Otherwise, the 'NO' checkbox should be marked with a cross.
9. Where in disclosing the invention the application refers to one or more micro-organisms deposited with a depository authority under the Budapest Treaty, then the 'YES' checkbox at paragraph 8 should be marked with a cross. Otherwise, the 'NO' checkbox should be marked with a cross. Attention is also drawn to the Fourth Schedule of the Patents Rules.
10. Where an agent is appointed, the fields for "DETAILS OF AGENT" and "ADDRESS FOR SERVICE IN SINGAPORE" should be completed and they should be the same as those found in the corresponding Patents Form 41. In the event where no agent is appointed, the field for "ADDRESS FOR SERVICE IN SINGAPORE" should be completed, leaving the field for "DETAILS OF AGENT" blank.
11. In the event where an individual is appointed as an agent, the sub-field "Name" under "DETAILS OF AGENT" must be completed by entering the full name of the individual. The sub-field "Firm" may be left blank. In the event where a partnership/body corporate is appointed as an agent, the sub-field "Firm" under "DETAILS OF AGENT" must be completed by entering the name of the partnership/body corporate. The sub-field "Name" may be left blank.
12. Attention is drawn to sections 104 and 105 of the Patents Act, rules 90 and 105 of the Patents Rules, and the Patents (Patent Agents) Rules 2001.
13. Applicants resident in Singapore are reminded that if the Registry of Patents considers that an application contains information the publication of which might be prejudicial to the defence of Singapore or the safety of the public, it may prohibit or restrict its publication or communication. Any person resident in Singapore and wishing to apply for patent protection in other countries must first obtain permission from the Singapore Registry of Patents unless they have already applied for a patent for the same invention in Singapore. In the latter case, no application should be made overseas until at least 2 months after the application has been filed in Singapore, and unless no directions had been issued under section 33 by the Registrar or such directions have been revoked. Attention is drawn to sections 33 and 34 of the Patents Act.
14. If the space provided in the patents form is not enough, the additional information should be entered in the relevant continuation sheet. Please note that the continuation sheets need not be filed with the Registry of Patents if they are not used.

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200205830-3

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Channel EqualisationField Of The Invention

- 5 The present invention relates to methods and apparatus for channel equalisation.

Background Of The Invention

- 10 In several fields of communication there exists the need to deal at a receiver with the time-varying nature of a communications channel.

One such field of communication is in code division multiple access (CDMA). CDMA is used in the cdmaOne system (IS-95) and will be used in the so-called third generation (3G) mobile system. Each of these systems uses direct-
15 sequence code division multiple access (DS-CDMA) in which orthogonal spreading codes are used on the down link to multiplex signals to multiple users. The spread signals of all users are combined together synchronously. The sum is scrambled by a long pseudo-noise code and transmitted from a base station. While the transmitted signals for the users within a cell are orthogonal to each
20 other, the multi-path propagation between the base station and the users seriously disrupts the orthogonality in the signals received by the mobile users. Interference between the signals for different users gives rise to what is known as multiple access interference (MAI). The presence of MAI significantly degrades system performance. Various methods have been proposed for suppressing MAI,
25 for example see:

Markku J Heikkila, "Interference Suppression in CDMA Down Link Through Adaptive Channel Equalisation", IEEE Vehicular Technology Conference 1999, which proposes a receiver algorithm which performs adaptive channel equalisation by estimating the transmitted chip sequence;

- 30 Stefan Werner, Jorma Lilleberg, "Down Link Channel D-Correlation in CDMA Systems With Long Codes", IEEE Vehicular Technology Conference 1999,

which proposes linear detectors which exploit the special signal structure of the downlink transmission;

Irfan Ghauri, Dirk T M Slock, "Linear Receivers for the DS-CDMA Down Link exploiting Orthogonality of spreading Sequences", IEEE Vehicular
 5 Technology Conference 1998, which proposes receivers which equalise for the estimated channel (based on the pilot signal) to render the user signals orthogonal, and a code matched filter is then used to cancel MAI for intra-cell users; and

Hooli, K, Latva-Aho, M and Juntti, M, "Multiple Access Interference
 10 Suppression With Linear Chip Equaliser in WCDMA Down Link Receivers", Globecom '99, which proposes receivers which equalise the channel (on the chip level, so that the system will work with long scrambling codes) prior to de-spreading, to restore orthogonality.

15 The suppression methods proposed in these papers are all based on the use of finite impulse response (FIR) equalisers.

If the input signal-to-noise ratio is high and if the channel is fixed, a FIR equaliser will usually achieve better performance than that achieved by a rake receiver.
 20 However, over a time-varying channel, where deep fading occurs the signal-to-noise ratio can be very low during the deep fading period. Adaptive algorithms do not work very well if there is a low signal-to-noise ratio. Although the periods of low signal-to-noise ratio caused by deep fading do not last long, the existence of low signal-to-noise ratio periods still degrades the convergence of adaptive
 25 algorithms. The faster the multi-path fading is, the more frequently low signal-to-noise ratio events occur, and the worse the adaptive equaliser performance becomes. Deep fading is one of the main reasons why adaptive equalisers perform so badly when used in conjunction with time-varying channels. Thus, the performance of an adaptive equaliser depends not only on the convergent speed,
 30 but also upon the instantaneous signal-to-noise ratio (rather than a mean signal-to-noise ratio).

When designing a FIR equaliser for a CDMA downlink receiver it is generally assumed that the equaliser should be as long as possible in order to produce a true inverse of the multi-path channel. Ghauri and Slock, cited above, state that “it is a well known result that longer equalizers give better results”. However we have appreciated that this assumption is based on the expectation that the true values for all weights can be obtained, whereas in a practical system the estimates for weights are noisy. Since the magnitude of every weight is different, the corresponding signal-to-noise ratio is also different. The estimation errors for small-value weights will be bigger than those for large-value weights in a noisy environment. Usually, small-value weights are also far from the centre of the FIR filter. When the instantaneous signal-to-noise ratio is high, the benefits brought about by the small-value weights is bigger than the sum of the estimation errors, and hence the overall effect will be good. Otherwise, that is where the instantaneous signal-to-noise ratio is low, the small-value weights will lead to a worse result and should be removed from the equaliser. So we have appreciated that the optimum equaliser length in a time-varying noisy environment is variable. We make use of this appreciation in the most preferred embodiments of the invention, where an adaptive order equaliser is used prior to de-multiplexing received CDMA signals which have been pre-processed to improve the signal to noise ratio. Other embodiments use fixed –order adaptive equalisers similarly.

Summary Of The Invention.

In a first aspect the present invention provides, in a wireless CDMA system in which orthogonal spreading codes are used, a method of reducing multiple access interference caused by a loss of orthogonality between multiple spread spectrum communications signals, the method comprising the steps of: receiving over a multi-path channel the multiple communications signals; passing the received signals through a plurality of correlation branches and combining the outputs of the correlator branches to produce a combined signal; passing the combined output signal through an adaptive equaliser to restore orthogonality; and demodulating the equaliser output.

The weights and length may be calculated in an adaptive loop which includes pilot demodulation. Such a solution was proposed in our co-pending Singapore patent application no. 200202669-8, the disclosure of which is hereby incorporated. The presence of an adaptive loop of this form makes it possible for the method to be
 5 operated without an additional channel estimation needed by common equalisers.

In a second aspect the invention provides a method of reducing multiple access interference between multiple communications signals, the method including the steps of: (i) receiving over a multi-path channel the multiple communications
 10 signals; (ii) recovering from the received signals a plurality of signals of interest each of which corresponds to a different one of the paths of the multi-path channel; (iii) estimating a weight for each of the paths of each of the signals recovered in step (ii); (iv) offsetting each of the recovered signals by an appropriate delay; (v) applying to each of the recovered signals a scale factor
 15 which is the conjugate of the corresponding weight estimated in step (iii); (vi) after carrying out steps (iv) and (v) on the recovered signals combining them to produce a combined signal; and (v) passing the combined signal through an equaliser to produce an output; and (vi) demodulating the equaliser output.

20 Preferably, between steps (v) and (vi) a step is carried out of calculating the weights and length in an adaptive loop which includes a pilot demodulator.

In a third aspect the invention provides an apparatus for use in a receiver in a communications system in which system signals are transmitted over multi-path
 25 channels, the apparatus including: means to recover from a signal received over one of said multi-path channels a plurality of signals of interest, each of the recovered signals corresponding to a different one of the paths of the one multi-path channels; means to estimate a weight for each of the paths of each of the recovered signals; means to offset each of the recovered signals by an
 30 appropriate delay; means to apply to each of the recovered signals a scale factor which is the conjugate of the respective weight; means to combine the recovered signals after their offsetting and scaling to produce an composite signal; an

equaliser to process the composite signal to produce an equalised signal; and means to de-spread and de-scramble the equalised signal to produce a desired signal.

- 5 Preferably the apparatus further comprises an adaptive loop which includes a pilot demodulator.

In a fourth aspect the invention provides a signal processor for a wireless receiver for use in a communications system in which the receiver receives signals
 10 transmitted over multi-path channels, including processing means to: recover from a signal received over one of said multi-path channels a plurality of signals of interest, each of the recovered signals corresponding to a different one of the paths of the one multi-path channels to estimate a weight for each of the paths of each of the recovered signals; to offset each of the recovered signals by an
 15 appropriate delay; to apply to each of the recovered signals a scale factor which is the conjugate of the respective weight; to combine the recovered signals after their offsetting and scaling to produce a combined signal; to provide an adaptive equalisation function for the processing of the combined signal and to produce therefrom an equalised signal; and to demodulate the equalised signal to recover
 20 a desired signal.

Preferably the adaptive equalisation function is provided using an adaptive loop which includes pilot demodulation.

25 In a fifth aspect the invention provides a mobile terminal for use in a CDMA communications system, the terminal including: a user interface adapted to allow a user to control the mobile terminal and to input local service signals for transmission and to hear remote speech signals recovered from received signals; a transmitter adapted to transmit the local service signals to a base station via a
 30 radio frequency transmit signal; and a receiver adapted to recover remote service signals from a received composite signal; the receiver including: a plurality of rake fingers to recover from a signal received over one of said multi-path channels a

plurality of signals of interest, each of the recovered signals corresponding to a different one of the paths of the one multi-path channels; means to combine the recovered signals from the rake fingers to produce a combined signal; an equaliser to process the combined signal and to increase the orthogonality thereof; and
 5 a demodulator to demodulate the output of the equaliser.

Preferably the receiver includes an adaptive loop for controlling the equaliser which includes a pilot demodulator.

10

Brief Description Of The Drawings

Embodiments of the present invention will now be described, by way of example only, with reference to the accompany figures in which:

Figure 1 is a schematic diagram of a cellular radio system;

15 Figure 2 is a functional block diagram of a known mobile terminal for use with the cellular radio system of Figure 1;

Figure 3 is a schematic diagram of a receiver according to an embodiment of the invention ;

20 Figure 4 is a schematic diagram showing how pre-combining is effected in a receiver according to an embodiment of the invention;

Figure 5 is a schematic diagram representing a variable length equaliser; and

Figure 6 is a plot comparing the bit error rate (BER) performance of receivers according to embodiments of the invention with that of known receiver
 25 designs;

Figure 7 shows how the diagram of Figure 3 is modified in the case that there are multiple antennas.

Detailed Description of Illustrative Embodiments.

30

Figure 1 is a schematic diagram of a cellular radio communications system 100 which uses a multiple access technology such as wideband CDMA (WCDMA) for

communication between users of user terminals 110 (for example, mobile phones) and base stations 120. A plurality of base stations 120, 120' and 120'' are provided for wireless communication with a greater plurality of mobile terminals, 110, 110', 110'', etc.. Signals from a mobile terminal 110 travel over free space on the uplink to base station 120. From the base station 120 the signals pass to a base station controller. The base station controller may be coupled to a mobile switching centre (MSC), and MSC may be connected in turn to a fixed communications network such as the public switched telephone network (PSTN) to permit signals from the user terminal 110 to be passed to a subscriber of the fixed network and vice versa. The fixed communications network will also carry signals from the user terminal 110 to other users or subscribers of the cellular radio system 100 as well as to users or subscribers of systems of other operators.

Signals from the mobile base station 120 travel also over free space on the downlink from the base station to the mobile terminals 110, 110', 110''. As with almost any radio communication system, there may be multi-path propagation of signals on the downlink between the base station 120 and the mobile terminal 110, for example 150, 150' and 150'', etc.. In particular some of the signals are reflected off buildings or other man made structures which are located between the base station 120 and the mobile terminal 110. Once signals have been reflected for a first time they may be reflected several times before finally reaching the user terminal 110. The presence of natural features such as hills and mountains may also contribute to multi-path propagation. Typically each different transmission path gives rise to a different delay. In general, each transmission path has a different length and different reflection conditions and this gives rise to different attenuation levels as well as different phase rotation. These different propagation properties disturb the orthogonality of the spreading codes for the users served by one base station. The loss of orthogonality gives rise to multiple access interference MAI. The same problem can arise both on the downlink and the uplink. In each case there is a challenge to the designer of the receiver in finding a workable solution to the problem of MAI. However, because of the constraints on the physical size, power consumption and cost of mobile terminals,

which do not apply to the same degree to base stations, the problem is greater with the design of the receiver for the mobile terminal.

Figure 2 is a functional block diagram of a conventional mobile terminal 110 for use with the cellular radio system of Figure 1. The mobile terminal 110 includes a micro controller unit (MCU) 200, an RF transceiver 202, a digital signal processor (DSP) 20, and a user interface 204. The mobile terminal 110 may also include external interfaces (not shown) for communication with a computer, LAN, or other device.

10

The RF transceiver 202 provides the wireless link between the mobile terminal 110 and a base station 120. The RF transceiver 202 has a receiver 210, here of conventional rake type, a transmitter 220, oscillator 230, an antenna coupling circuit 240 and an antenna 250. The antenna is coupled to the transmitter 220 and receiver 210 by the antenna coupling circuit 240 which includes a duplexer for isolating transmit and receive signal paths during full duplex transmit and receive operations. Half duplex operations may also be supported and a switch provided to multiplex the antenna 250 between the transmitter 220 and the receiver 210. Transmit band and receive band filters in the duplexer provide the necessary isolation between the transmit and receive signal paths in the transceiver 202.

20

The RF signal is first amplified by amplifier 245, down converted by unit 260 to a base band signal, filtered by a filter 270 and A/D converted by an analogue/digital converter 275. The output of the converter 275 is sent to the Rake receiver 210 to process the CDMA downlink signal from the base station 120. Conversely, the transmitter 220 receives base band signals from the DSP 203, amplifies them and then uses the amplified signals to modulate an RF carrier to send as uplink signals to the base station 120. The oscillator 230 provides reference signals used for frequency translation in the receiver 210 and in the transmitter 220.

30

The DSP 203 includes a digital modem 280 and a source coder/decoder 290. Source coder/decoder 290 includes a speech coder for digitising and coding

speech for transmission on the uplink, as well as for decoding speech signals received on the downlink, converting them to audio signals to be output over the user interface 204.

- 5 The digital modem 280 processes digital signals to make communications between the mobile terminal 110 and the base station 120 more robust.

The micro controller unit 200 supervises the operation of the mobile terminal 110 and administers the procedures associated with the communication protocol being
10 used. The MCU 200 typically includes a microprocessor and supporting peripherals facilitating control of the mobile terminal. Usual peripherals include timer/counter units, pulse-width modulations (PWM) generators, bi-directional input/output, serial communications interfaces and other modules to permit the microprocessor to interface flexibly with the various sub elements and circuitry of
15 the mobile terminal 110. The microprocessor may include hardware computational units to speed up mathematical operations such as squaring and division.

The MCU will generally use timers to track network time, using that information to assist in synchronising the mobile terminal 110 with the required transmit and
20 receive signal timing. The timing information may be used to identify frame boundaries and slot indexes associated with a received CDMA signal. The MCU also uses timers to trigger specific tasks as the mobile terminal 110 switches between different operating modes (such as sleep, receive, talk, standby etc.). The MCU also uses register files in memory to store calibration data, the
25 electronic serial number (ESN) of the user (which may be used in authentication or for other security purposes), and other information needed on a longer term basis.

The MCU 200 also implements the communications protocols used by the mobile
30 terminal 110. The communications protocol specifies timings, multiple access approach, modulation format, frame structure, power level, as well as many other aspects of the operation of the mobile terminal 110. The MCU 200 inserts

signalling messages into the transmitted signals sent to the base station 120 and extracts signalling messages from the received signals. The MCU 200 responds to signalling messages received from the base station 120 in accordance with the relevant communications protocol. When the user inputs commands by the user interface 204, the commands are sent to the MCU 200 for action. The MCU also provides power management to the mobile terminal 110, including monitoring battery energy level, charging the battery, and minimising power consumption by changing modes. In general there are three modes of operation for the mobile terminal: idle, receive and talk. In idle mode the MCU 200 deactivates most functions except the digital system clock. In receive mode the MCU activates the receiver 210 and the digital modem 280. It further demodulates the paging channel in the received CDMA signal until it receives a valid paging message and then switches to talk mode. In talk mode the entire mobile terminal is activated to support two-way communication.

15

The MCU and the DSP use dedicated or shared buses to connect to memory (not shown). The memory is typically segmented into blocks that hold the start-up code, control software, DSP firmware, and temporary data, etc.

20 In mobile units according to embodiments of the invention the conventional rake receiver 210 is replaced with a receiver which uses pre-combining followed by an equaliser based signal demodulator (shown as 410 in Figure 2). The pre-combining portion of the receiver can use a conventional rake arrangement but without demodulation within the rake fingers. Preferably the equaliser which follows the pre-combining portion is an adaptive order equaliser in which the order of the equaliser is adjusted adaptively according to the calculated error signal for each order. This approach will be discussed further with reference to Figures 3 and 4.

30 The structure of the new receiver 410 is shown in Figure 3. The new receiver has three main sections: the pre-combining stage, 420; the normalisation stage 430; and the equalisation stage 440. Although Figure 3 shows the equalisation stage

as including an adaptive order equaliser, the invention can also be implemented using a fixed α -order equaliser.

The pre-combining stage 420 is generally similar to a conventional rake receiver
 5 except that the received signals are not subject to de-spreading here (except to estimate path weights) : de-spreading (demodulation) occurs in the equalisation stage 440.

Within the pre-combining stage 420 there are several parallel fingers 450¹ to 450^L.
 10 Within the multi-path composite signal received from the base station 120 several strong paths are identified and appropriate path weights are estimated using a sliding window or an exponential weighted average approach. Within each finger 450 the received signal is subjected to a different delay, the delays being decided by a module 460 as in a rake receiver. In contrast to known receivers, the fingers
 15 do not include CDMA demodulators.

The received signal is passed to the fingers 450 of the pre-combining stage which are associated with the strong paths identified. In each finger the path weight is estimated by de-scrambling and de-spreading the pilot signal, then forming an
 20 exponential weighted average or sliding window average. The conjugate of the average results is then calculated to give the estimated weight for the relevant path. The received signal is multiplied by the estimated path weights, but is not at this stage subject to de-scrambling and de-spreading as it would be in a conventional rake receiver. The outputs of all the fingers 450¹ to 450^L are then
 25 added together in adder 490 to provide the output of the pre-combining stage. Each finger 450 includes a delay-offset unit 470 and a scale factor (the conjugate of the relevant path weight) is also applied, at 480, to the signal in that branch. Within this pre-combining stage 420 of the receiver 410 there is no de-spreading of the signal symbols as there is in a rake receiver (except for that used in
 30 estimating the path weights). The pre-combining smoothes the signal and improves the signal to noise ratio because of the multi-path diversity. The pre-combining stage is preferably followed by a normalization stage 430 because the

pre-combining process changes the magnitude of the signal. The usual automatic gain controller (AGC), which is provided in the receiver up-stream of the pre-combiner is unable to compensate for the change in signal magnitude caused by the pre-combiner as this happens downstream of the pre-combiner. The
 5 normalisation stage takes the instantaneous magnitude of the signal output by the pre-combining stage, at part 500, the exponential weighted average of the magnitude is calculated, at 510, and this is used to normalise the signal at 520.

The output of the normalisation stage 430 is then passed to the equalisation stage
 10 440 and the signal is de-spread (demodulated).

The equaliser stage 440 receives the output of the normalisation stage 430 which is passed through a finite impulse response (FIR) filter 600. In the example shown an adaptive order equaliser is used. The order of the equaliser is adjusted
 15 according to the calculated error signal for each order. In a preferred embodiment the equaliser is preferably adjusted according to a recursive least square (RLS) algorithm such as that developed below. Of course, other approaches to order adaptation can be used. One such alternative approach is steepest descent (SD). An algorithm to apply SD is also developed below.

20

The equaliser 440 uses the CDMA pilot signal for adaptation. The equaliser 440 processes the pre-combined signals output by the normalisation stage to produces a chip estimate. The chip-rate output 625 of the equaliser is de-spread to reveal pilot symbols 615 and these are compared with the known pilot
 25 sequence symbols 620. The difference is then used as an error signal 640 in an adaptive algorithm such as RLS. The weights in the FIR filter are controlled in accordance with the adaptive algorithm, according to conventional methods.

The desired user's channel is recovered by de-spreading and de-scrambling 650
 30 the output of the equalizer. The desired traffic symbols are recovered at an output 660.

The pre-combining process is advantageous irrespective of the type of adaptive equaliser which follows it. So, it can be followed by a fixed order adaptive equaliser or an adaptive order adaptive equaliser. However, if pre-combination is used with an adaptive order equalizer considerable performance improvements can be achieved if the length of the equalizer is appropriately adjusted. As already mentioned, there is an optimum length for an FIR equalizer in a noisy environment and the optimal length varies with the properties of the channel which is being equalized. In general, order optimisation is best achieved using "minimum output square-error". By pre-combining and using adaptive order adjustment the performance of the receiver of Figures 2 to 4 is considerably improved compared to common adaptive order equalizers.

The variable order FIR filter is shown in Figure 5. Let the length of the FIR filter be $2m - 1$. Then, from Figure 5, we can see that m can be changed from 1 to $M+1$. That is, the shortest filter has only 1 weight, while the longest filter has $2M+1$ weights. The length of the filter is determined by the adaptive algorithm which is described below. Let the transmitted multi-user chip sequence from a base station be:

$$x(n) = spm(n) \sum_m \sum_{k=1}^K \alpha_k b_k(m) c_k(n - mN), \quad (1)$$

where α_k , $b_k(m)$ and $c_k(n)$ are the amplitude, the data and the spreading code for user k in one cell. The period of the spreading code $c_k(n)$ is equal to the processing gain, N . The signals of all users in a cell are combined synchronously and scrambled by an aperiodic random code $spm(n)$.

The multi-path fading channel is modeled as a MA (moving average) process and the received signal can be represented as a received signal vector:

$$y(n) = Tx(n) + \xi(n) \quad (2)$$

where,

$$\mathbf{y}(n) = [y(nN+1) \cdots y(nN+N+L-1)]^T \quad (3)$$

is the received signal vector consisting of the signal samples ,

$$\mathbf{x}(n) = [x(nN-L+2) \cdots x(nN+N+L-1)]^T, \quad (4)$$

$$\xi(n) = [\xi(nN+1) \cdots \xi(nN+N+L-1)]^T \quad (5)$$

is a white Gaussian noise vector. and

$$\mathbf{T} = \begin{bmatrix} h(L) \cdots h(1) 0 0 \cdots 0 \\ \vdots & & \vdots \\ \vdots & & \vdots \\ 0 \cdots 0 0 h(L) \cdots h(1) \end{bmatrix} \quad (6)$$

is the L-path channel matrix, $()^T$ denotes transpose in this specification.

10 For pre-combining, we reorganize the received signal vector as a data matrix as

$$\mathbf{Y}_{pre} = \begin{bmatrix} y(nN+1) \cdots y(nN+N) \\ \vdots \\ y(nN+L) \cdots y(nN+L+N-1) \end{bmatrix}, \quad (7)$$

and construct a channel vector as

$$\mathbf{L} = [\hat{h}(L) \cdots \hat{h}(1)]^T \quad (8)$$

where, $\hat{h}(l)$ denotes the estimation of the path l , which can be obtained by the

15 same method as in a rake receiver.

Then the output of the pre-combining block or the input of the equalizer can be written as:

$$\mathbf{Y}_{equ} = \mathbf{Y}_{pre}^T \mathbf{L}^* \in C^{N \times 1} \quad (9)$$

20 The \mathbf{Y}_{equ} consists of $y_{equ}(i)$, $y_{equ}(i+1) \dots$ After which, the data matrix can be constructed using the output of the pre-combining process as:

$$\mathbf{Y}(n) = \begin{bmatrix} y_{equ}(nN - M + 1) \cdots y_{equ}(nN + N - M) \\ \vdots \\ y_{equ}(nN + 1) \cdots y_{equ}(nN + N) \\ \vdots \\ y_{equ}(nN + M + 1) \cdots y_{equ}(nN + N + M) \end{bmatrix} \quad (10)$$

Constructing the de-scrambling/de-spreading vector of user k :

$$\mathbf{C}_k(n) = \text{diag}(spm(n,1) \cdots spm(n,N)) \cdot \mathbf{c}_k \quad (11)$$

5

where $\mathbf{c}_k = [c_{k1} \cdots c_{kN}]^T$ is the spreading code waveform of user k . $spm(n, j)$ is the j th chip value in n th symbol of the aperiodic random code. Let \mathbf{c}_1 denotes the spreading code for the pilot channel.

10 Then the algorithm is as follows:

--Initialize $\mathbf{w}(0) = \mathbf{0} \in C^{(2M+1) \times (M+1)}$, $forget_w = a$, $forget_R = b$,
 $\mathbf{eo} = \mathbf{0} \in C^{(M+1) \times 1}$

$$\hat{\mathbf{b}}(n) = \mathbf{w}(n)^H \mathbf{Y}(n) \mathbf{C}_1(n)^* \in C^{(M+1) \times 1} \quad (12)$$

$$\mathbf{e}(n) = \text{pilot} - \hat{\mathbf{b}} \in C^{(M+1) \times 1}(n) \quad (13)$$

15 where $e_m(n)$ is the m th element in the vector $\mathbf{e}(n)$ and a and b are selected parameters called forgetting factors, which can be chosen according experimentally as in many conventional systems. Their value ranges from 0 to 1.

--For optimal order selection:

$$\mathbf{eo}(n) = forget_w \cdot \mathbf{eo}(n-1) + (1 - forget_w) \cdot \mathbf{e}(n), \quad (14)$$

20

$$m_o(n) = \arg \min_i \{eo_i, i = 1, \dots, M+1\} \quad (15)$$

where, eo_i means the i th element of the vector $\mathbf{eo}(n)$.

--The optimal length of the equalizer at instant n is $m_o(n)$. Therefore the output traffic symbol at instant n should be

$$\hat{b}_k(n) = \mathbf{w}_{m_o}(n)^H \mathbf{Y}(n) \mathbf{C}_k(n)^* \quad (16)$$

25 where, $\mathbf{w}_m(n)$ means the m th column of the matrix $\mathbf{w}(n)$.

For $m = 1$ to $M + 1$, let

$$\mathbf{dim}_m = \text{diag}\left(\underbrace{0, \dots, 0}_{M-m+1}, \underbrace{1, \dots, 1}_{2m-1}, \underbrace{0, \dots, 0}_{M-m+1}\right), \text{ and}$$

$$\mathbf{chn_vec}_m = \mathbf{dim}_m \mathbf{Y}(n) \mathbf{C}_k(n)^*$$

5

--For the steepest-descent (SD):

$$\mathbf{w}_m(n+1) = \mathbf{w}_m(n) + \text{Forget_R} \cdot \mathbf{chn_vec}_m \cdot e_m(n)^* \quad (17)$$

--For recursive least square (RLS):

$$\mathbf{w}_m(n+1) = \mathbf{w}_m(n) + \mathbf{R}_m^{-1}(n) \cdot \mathbf{chn_vec}_m \cdot e_m(n)^* \quad (18)$$

10 where

$$\mathbf{R}_{m,2m-1}^{-1}(n+1) = \mathbf{R}_{m,2m-1}^{-1}(n) - \frac{\mathbf{R}_{m,2m-1}^{-1}(n) \mathbf{chn_vec}_{m,2m-1} \cdot \mathbf{chn_vec}_{m,2m-1}^H \mathbf{R}_{m,2m-1}^{-1}(n)}{1 + \mathbf{chn_vec}_{m,2m-1}^H \cdot \mathbf{R}_{m,2m-1}^{-1}(n) \cdot \mathbf{chn_vec}_{m,2m-1}} \quad (19)$$

and where

$$\mathbf{R}_m^{-1}(0) = \begin{bmatrix} 0 & 0 & 0 \\ 0 & \underbrace{\delta \mathbf{I}}_{2m-1} & 0 \\ 0 & 0 & 0 \end{bmatrix} \quad (20)$$

$\mathbf{R}_{m,2m-1}^{-1} \in C^{(2m-1) \times (2m-1)}$ is a matrix consisting of the elements

15 from row $M - m + 2$ to row $M + m$ and from column $M - m + 2$ to column $M + m$ of

\mathbf{R}_m^{-1} , similarly, $\mathbf{chn_vec}_{m,2m-1} \in C^{(2m-1) \times 1}$ is a vector consisting of the elements from row $M - m + 2$ to row $M + m$ of $\mathbf{chn_vec}_m$, while δ is a large positive real number.

20 Figure 6 illustrates the bit error rate performance of a conventional rake receiver, a receiver based on a fixed-order equaliser without pre-combining, and two receivers using pre-combining and according to the invention : one with a fixed – order adaptive equaliser and the other with an adaptive-order equaliser whose order is adapted according to the RLS algorithm developed above. Fig. 6 is a

performance simulation result defined by parameters selected according to the HSDPA (high-speed downlink packet access) recommendations for WCDMA systems. The multipath fading channel is a 4-path channel with Doppler frequency 6Hz, and the forgetting factors are $a=0.9$ and $b=0.98$.

5

Fig. 7 shows how the structure shown in Fig. 3 is varied in a second version of the embodiment, which includes multiple antennas (a number N_u). In this case, there is an antenna branch for each of the antennas. The antenna branch includes a pre-combining stage 420, a normalisation stage 430, an equalisation stage 440,
 10 and a de-spreader and de-scrambler unit 650 (demodulator). Each of these stages are equivalent to the corresponding stages shown in Fig. 3, but the pilot de-spreading results for each antenna branch are added to the results of the same process in the other antenna branch(es). The sum of the pilot de-spreadings is compared with the "reference" to generate the error signal 640. The
 15 error signal 640 is used by a single adaptive algorithm RLS or SD module 605 to calculate the weights and optimal length of the respective FIR equaliser 600 for each antenna branch. The outputs of the demodulators 650 are combined by an adder 500.

20 The process is as follows. This is equivalent to the mathematical description given above in the case that the number of antennas, $N_u=1$.

a) In each antenna branch, received signal after pre-combining and normalization is fed to the variable length (or fix length) FIR equalizer, the
 25 equalization output is sent to desired signal de-modulator (de-scrambling plus de-spreading in WCDMA system) and pilot de-modulator (de-scrambling in WCDMA system).

b) The pilot de-modulation outputs from all antenna branches are added.
 30 The sum is subtracted from the "reference signal". The subtraction results is so-called "error signal". If the variable length equalizer is adopted, the "error signal" will be calculated $M+1$ times corresponding to different equalizer lengths in one

iteration, and the $M+1$ "error signals" are exponential weighted average with their previous values, respectively. Please see equation (14) in the draft. In equation (14), \mathbf{e} is a column vector whose elements are the $M+1$ error signals respective. \mathbf{eo} is also a column vector that includes $M+1$ results of the exponential weighted average of the error signals corresponding to different equalizer lengths. In (14), $forget_w$ can be chosen in the range from 0 to 1. The exact value of $forget_w$ depends on the application environment. Simultaneously, the index for minimum exponential weighted error signal is selected and kept following the rule in equation (15). If the fixed length equalizer is used, the error signal will only be calculated one time, and this error signal is kept. It can also be represented by (14). In this case, $forget_w=0$, and there is just one element in \mathbf{e} and \mathbf{eo} .

c) The adaptive algorithm will calculate the weights and optimal length for equalizers in all antenna branches using the exponential weighted error signals and others described below. The calculating algorithm can be steepest-descent (SD) in equation (17) or recursive least square (RLS) in equation (18).

If SD is used, the current weights for equalizers are determined by the exponential weighted error signal (\mathbf{eo}), the channel vectors ($\mathbf{chn_vec}_m$), forgetting vector \mathbf{b} and the weights last time. $\mathbf{chn_vec}_m$ is formed by the product of a window function \mathbf{dim}_m (show in p15, line 6 in the draft) and despread results for pilot (\mathbf{YC}_1) \mathbf{dim}_m admits $2m-1$ elements in the central of the $\mathbf{chn_vec}_m$ and disable the other elements. This is the necessity for choosing the optimal equalizer length (order). Index m should be chosen from 1 to $M+1$ corresponding to $M+1$ different equalizer lengths if the variable length equalizers are used. In every antenna branch, this calculation is the same.

If RLS is used, the current weights for equalizers are determined by the exponential weighted error (\mathbf{eo}), the channel vectors ($\mathbf{chn_vec}_m$), pseudo-inverse \mathbf{R}_m^{-1} forgetting vector \mathbf{b} and the weight last time. $\mathbf{chn_vec}_m$ is formed by the same way in SD algorithm. The difference is calculation of the pseudo inverse

\mathbf{R}_m^{-1} . The pseudo inverse can be calculated using equations (19) and (20) recursively. In every antenna branch, this calculation is the same.

By the above process, the equalizer weights and the optimal length (order) for the
5 equalizer are calculated recursively.

Also, by the above process, we have obtained the optimal equalizer length " m_o " and the corresponding weights $\mathbf{W}m_o$ for each antenna branch. Therefore the output of the detected user signal is shown in equation (16).

10

Many variations are possible within the scope of the invention. For example, although the advantages are explained above of including an adaptive loop for the equaliser, particularly one incorporating pilot demodulation, this not necessary for operation of certain embodiments of the invention.

15

CLAIMS

1. In a wireless CDMA system in which orthogonal spreading codes are used, a method of reducing multiple access interference caused by a loss of
5 orthogonality between multiple spread spectrum communications signals, the method comprising the steps of:
 - (a) receiving over a multi-path channel the multiple communications signals ;
 - (b) passing the received signals through a plurality of correlation
10 branches and combining the outputs of the correlator branches to produce a combined signal;
 - (c) passing the combined output signal through an adaptive equaliser; and
 - (d) demodulating the equaliser output.
- 15 2. A method according to claim 1 further including adapting the equaliser using an adaptive loop including pilot de-modulation.
3. A method of reducing multiple access interference between multiple
20 communications signals, the method including the steps of:
 - (i) receiving over a multi-path channel the multiple communications signals ;
 - (ii) recovering from the received signals a plurality of signals of interest each of which corresponds with a different one of the paths of the multi-path
25 channel;
 - (iii) estimating a weight for each of the paths of each of the signals recovered in step (ii);
 - (iv) offsetting each of the recovered signals by an appropriate delay;
 - (v) applying to each of the recovered signals a scale factor which is the
30 conjugate of the corresponding weight estimated in step (iii);
 - (vi) after carrying out steps (iv) and (v) on the recovered signals combining them to produce a combined signal; and

(v) passing the combined signal through an equaliser to produce an output; and

(vi) demodulating the equaliser output.

5 4. A method according to claim 3 further including adapting the equaliser using an adaptive loop which includes pilot demodulation.

5. A method as according to any preceding claim, wherein a normalisation process is carried out prior to passing the combined signal through the equaliser.

10

6. A method as claimed in claim 5, wherein the normalisation process involves taking an exponential weighted or slide window average of the combined signal.

15 7. A method as claimed in any one of the preceding claims, wherein the equaliser is an adaptive order equaliser whose length is adaptively adjusted.

8. A method as claimed in claim 7, wherein the order of the equaliser is adjusted according to an adaptive order algorithm of which the optimisation
20 criterion is minimum mean square error.

9. A method as claimed in any one of the preceding claims, wherein the equaliser applies a recursive least square algorithm.

25 10. Apparatus for use in a receiver in a communications system in which system signals are transmitted over multi-path channels, the apparatus including:

means to recover from a signal received over one of said multi-path channels a plurality of signals of interest, each of the recovered signals corresponding to a different one of the paths of the one multi-path channels;

30 means to estimate a weight for each of the paths of each of the recovered signals;

means to offset each of the recovered signals by an appropriate delay;

means to apply to each of the recovered signals a scale factor which is the conjugate of the respective weight;

means to combine the recovered signals after their offsetting and scaling to produce an composite signal;

5 means to normalise the composite signal;

an equaliser to process the composite signal to produce an equalised signal; and

means to demodulate the equalised signal to produce a desired signal.

10 11. Apparatus according to claim 10 further including an adaptive loop which includes a pilot demodulator.

12. A signal processor for a wireless receiver for use in a communications system in which the receiver receives signals transmitted over multi-path
15 channels, including

processing means to:

recover from a signal received over one of said multi-path channels a plurality of signals of interest, each of the recovered signals corresponding to a different one of the paths of the one multi-path channels

20 to estimate a weight for each of the paths of each of the recovered signals;

to offset each of the recovered signals by an appropriate delay;

to apply to each of the recovered signals a scale factor which is the conjugate of the respective weight;

25 to combine the recovered signals after their offsetting and scaling to produce a combined signal;

to normalise the combined signal;

to provide an adaptive equalisation function for the processing of the combined signal and to produce therefrom an equalised signal; and

30 to demodulate the equalised signal to recover a desired signal.

13. A signal processor according to claim 12 in which the processing means is arranged to adapt the adaptive equaliser using an adaptive loop including pilot demodulation.

5 14. A mobile terminal for use in a CDMA communications system, the terminal including:

a user interface adapted to allow a user to control the mobile terminal and to input local service signals for transmission and to hear remote service signals recovered from received signals;

10 a transmitter adapted to transmit the local service signals to a base station via a radio frequency transmit signal; and

a receiver adapted to recover remote service signals from a received composite signal; the receiver including:

a plurality of rake fingers to recover from a signal received over one of
15 said multi-path channels a plurality of signals of interest, each of the recovered signals corresponding to a different one of the paths of the one multi-path channels;

means to combine the recovered signals from the rake fingers to produce a combined signal;

20 an equaliser to process the combined signal and to increase the orthogonality thereof; and

a demodulator to demodulate the output of the equaliser.

15. A mobile terminal according to claim 14 further including an adaptive loop for
25 adapting the equaliser and including pilot demodulation.

ABSTRACT

Channel Equalisation

- 5 A method of reducing multiple access interference in a CDMA radio system uses a plurality of rake fingers to pre-process received signals but without demodulation or de-spreading. The outputs of the rake fingers are combined, normalised and then processed by an adaptive equaliser to recover orthogonality lost in transmission. The equaliser output is then subject to de-spreading and de-
- 10 scrambling to recover the desired user signals. The equaliser is preferably adaptive order, the length being adapted in dependence upon the instantaneous signal to noise ratio.

Figure 3

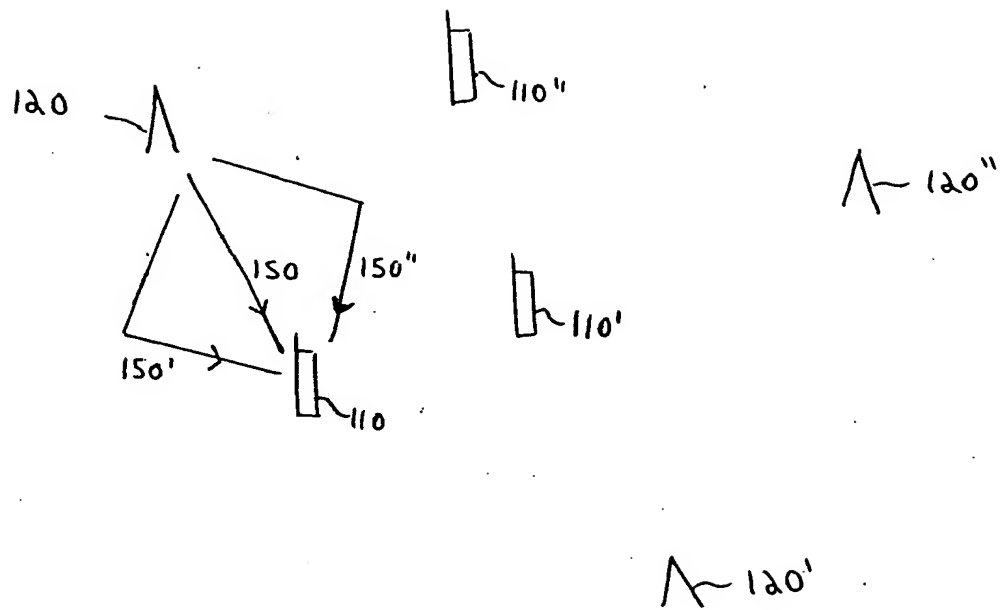


Fig. 1

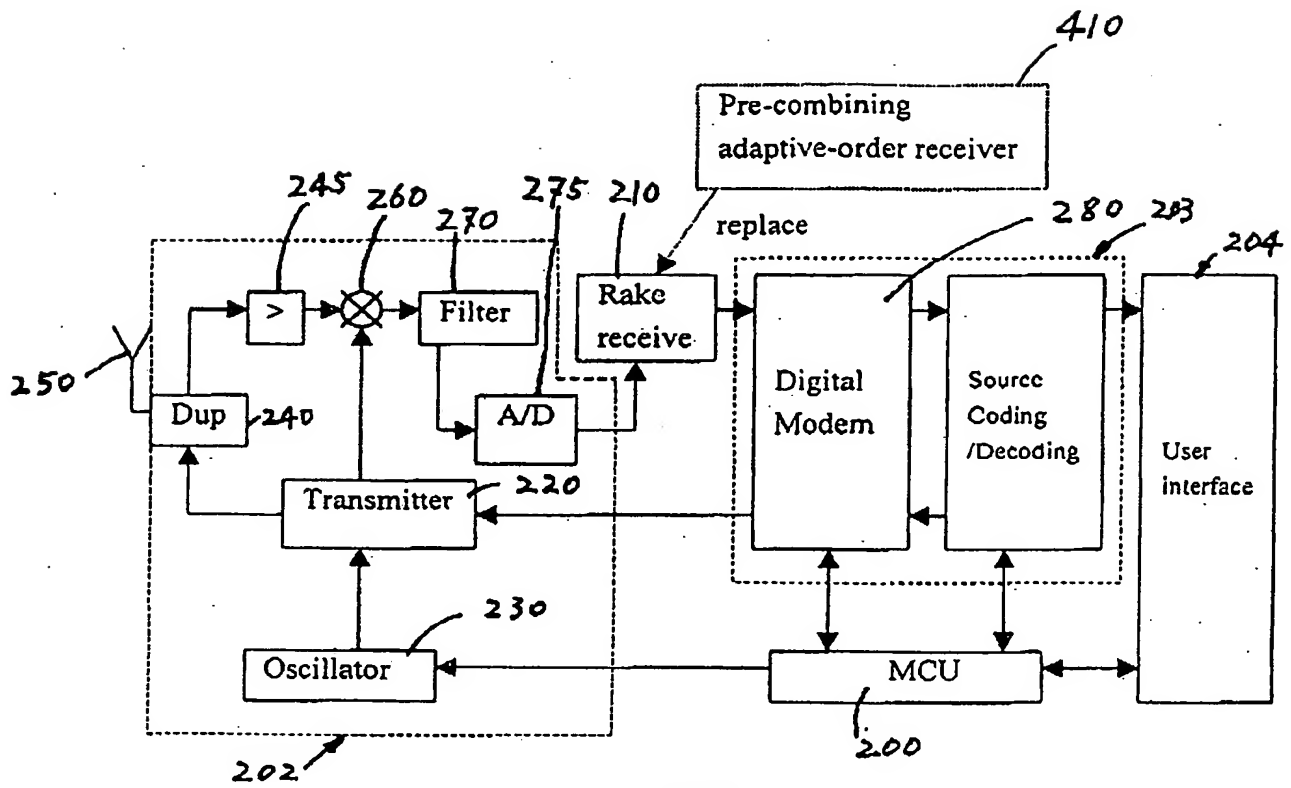


Fig.2

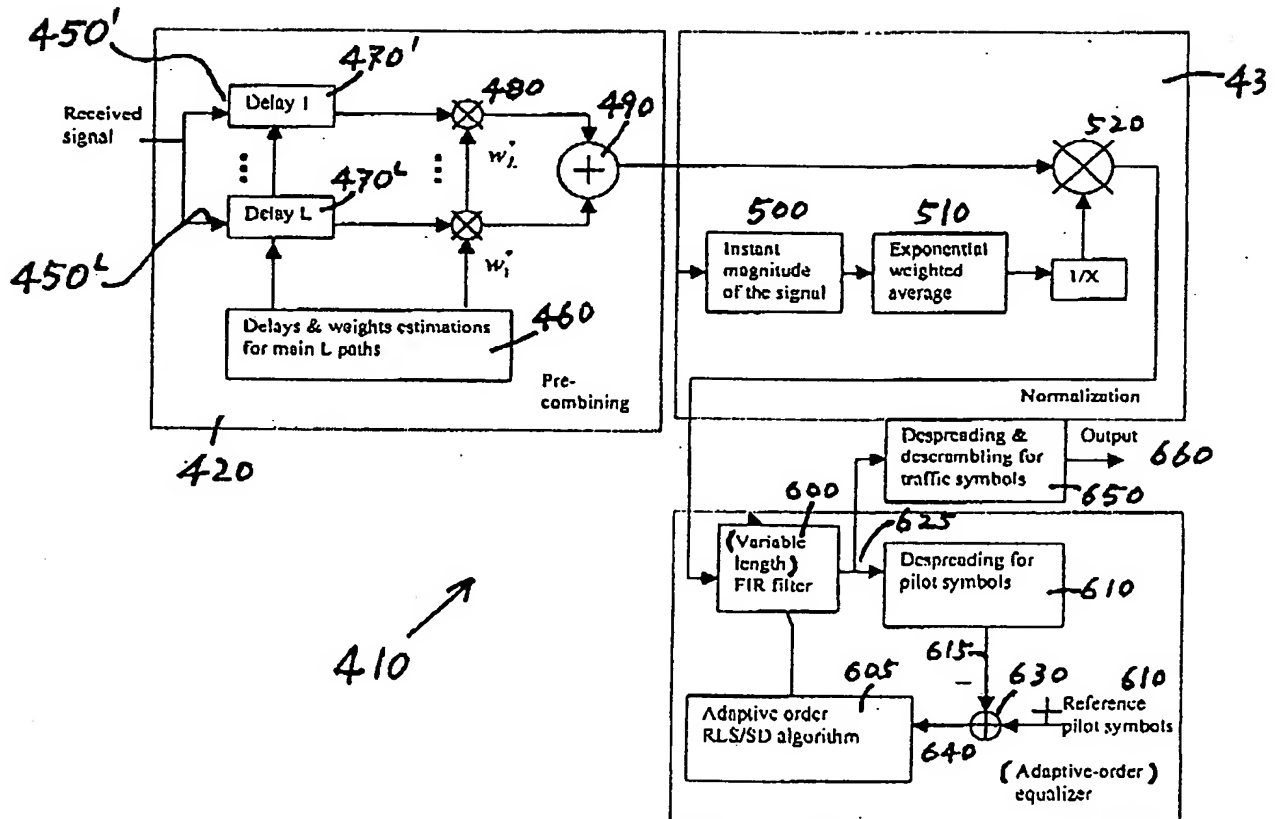


Fig.3

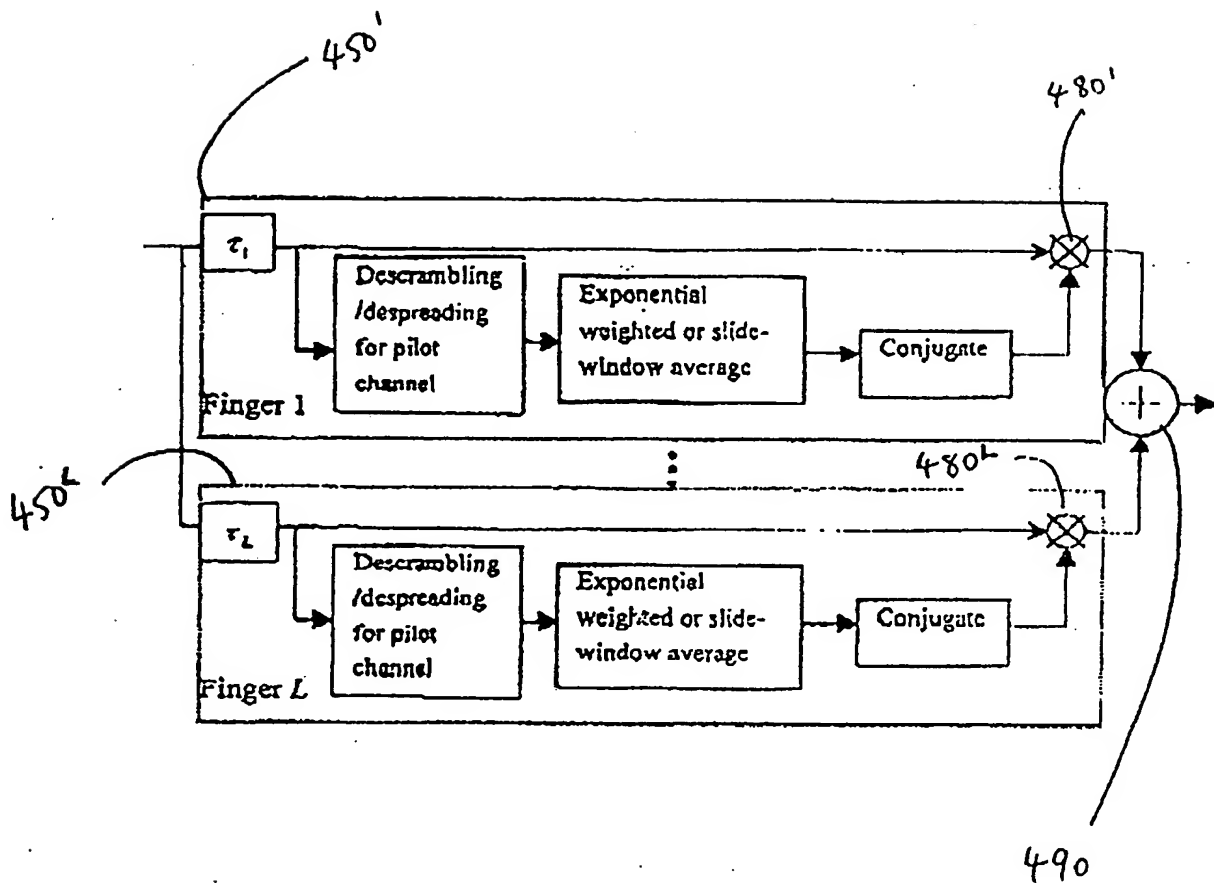


Fig. 4

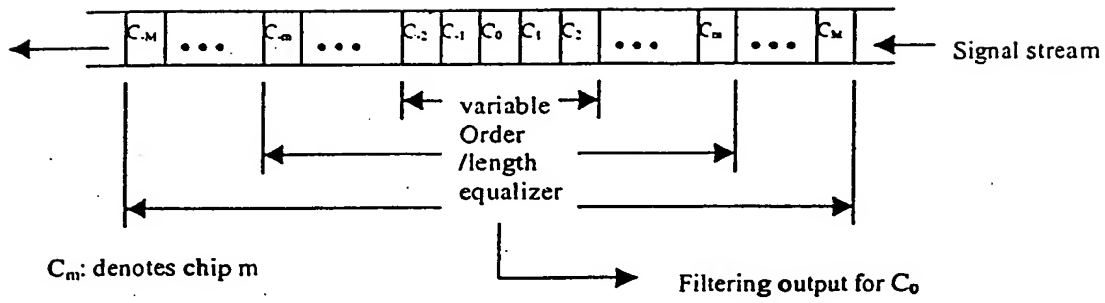


Fig. 5

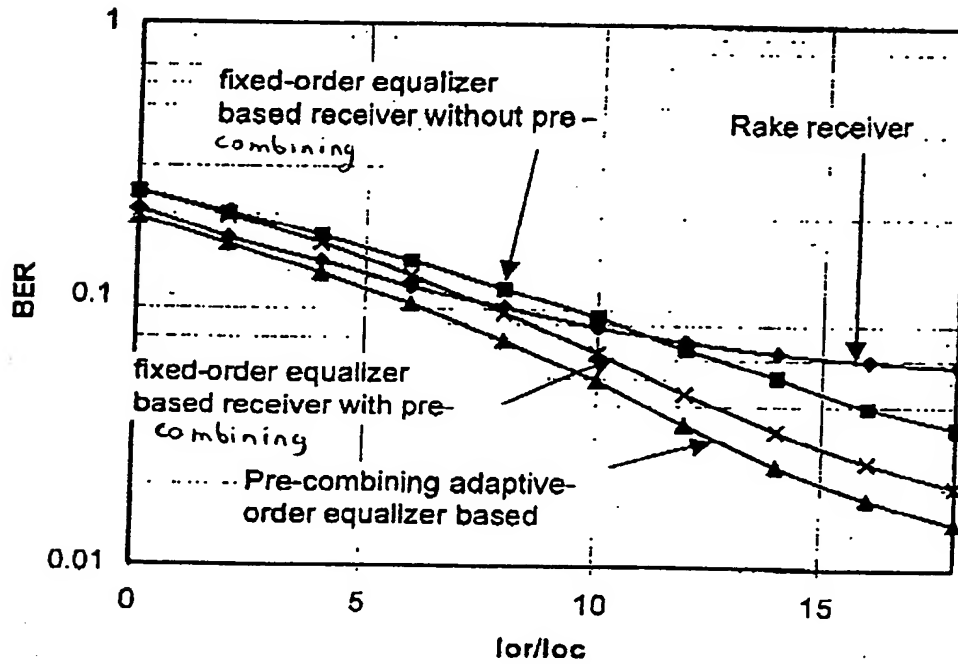


Fig.6.

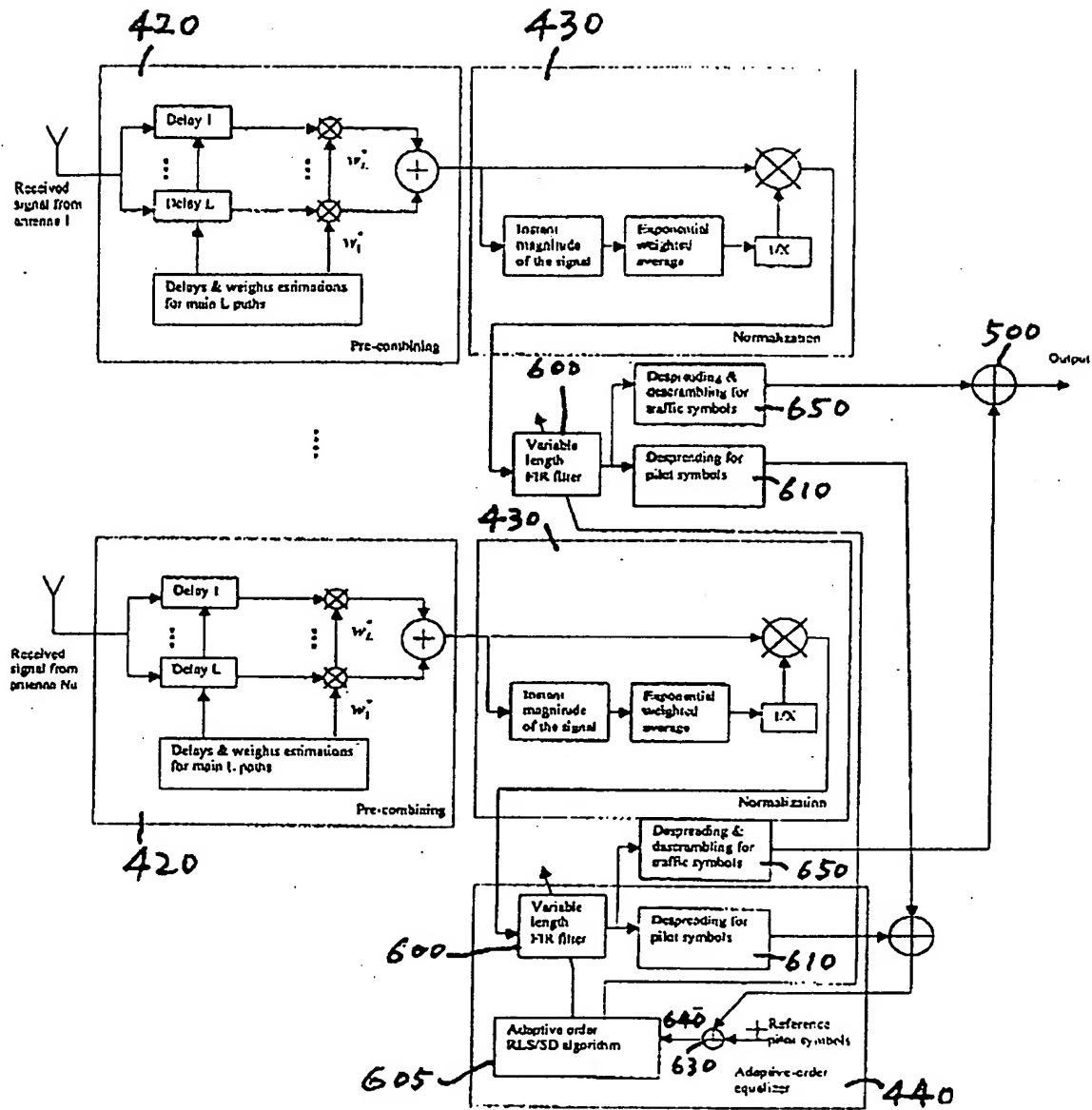


Fig.7